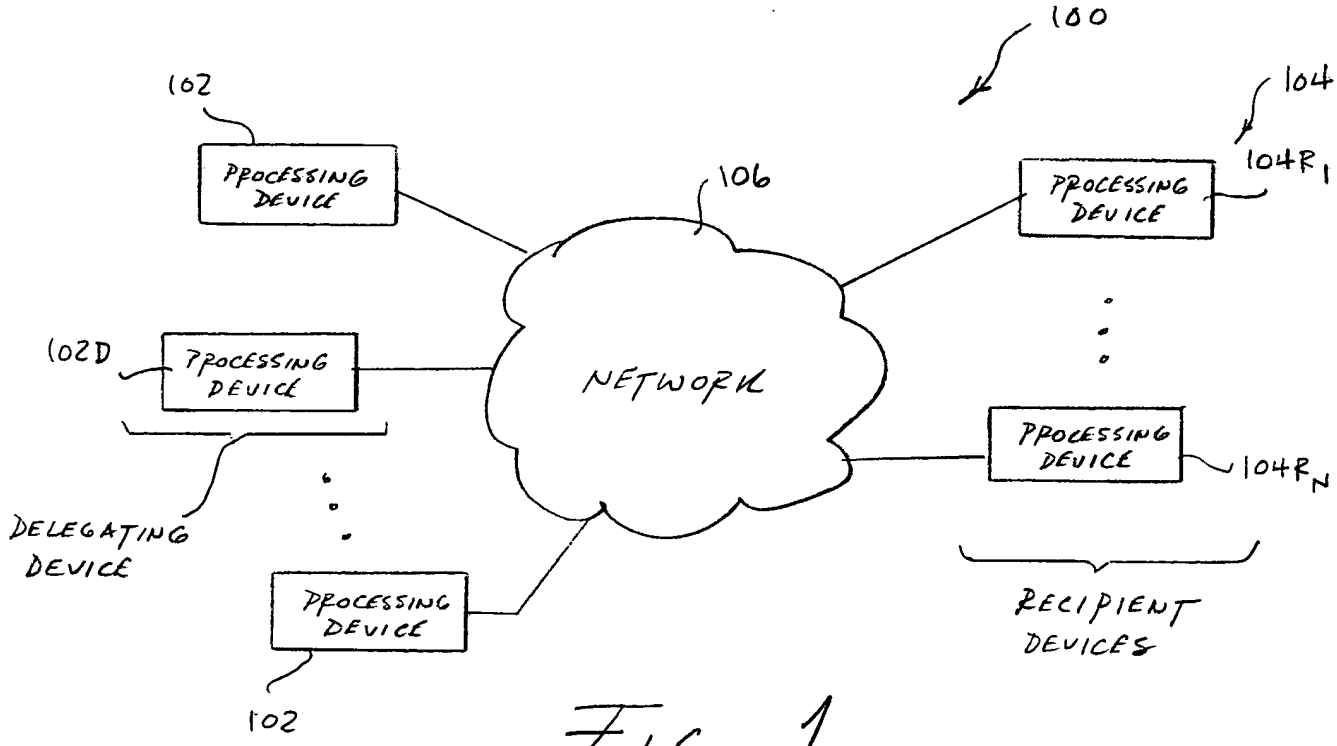
100
102
PROCESSING DEVICE
106
NETWORK
104
104R1
PROCESSING DEVICE
102D
PROCESSING DEVICE
DELEGATING DEVICE
PROCESSING DEVICE
104RN
PROCESSING DEVICE
RECIPIENT DEVICES
102

FIG. 1

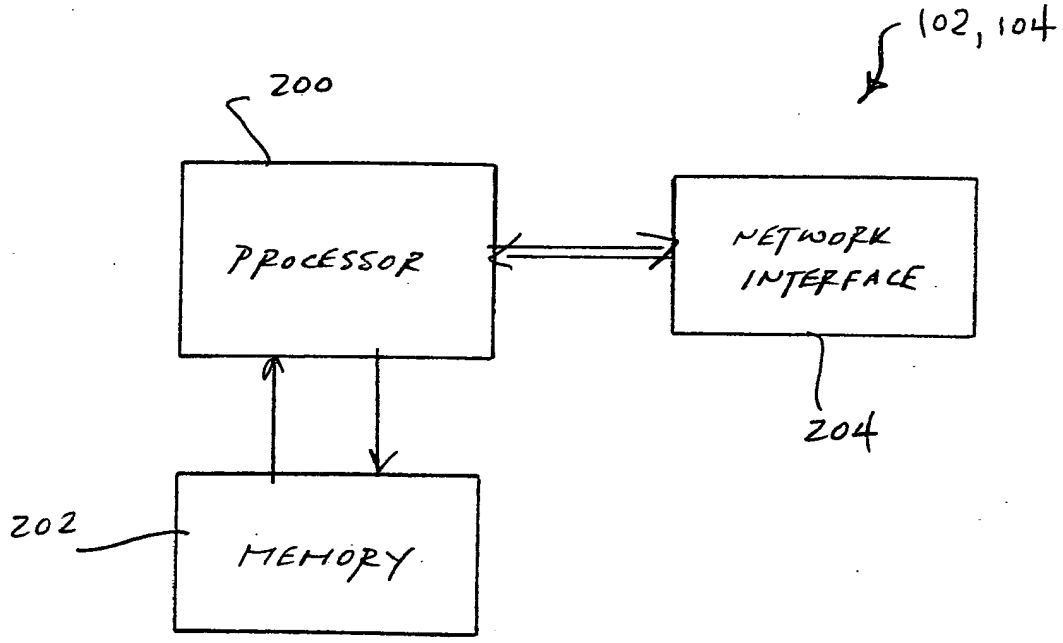
102, 104
200
PROCESSOR
NETWORK INTERFACE
204
202
MEMORY

FIG. 2

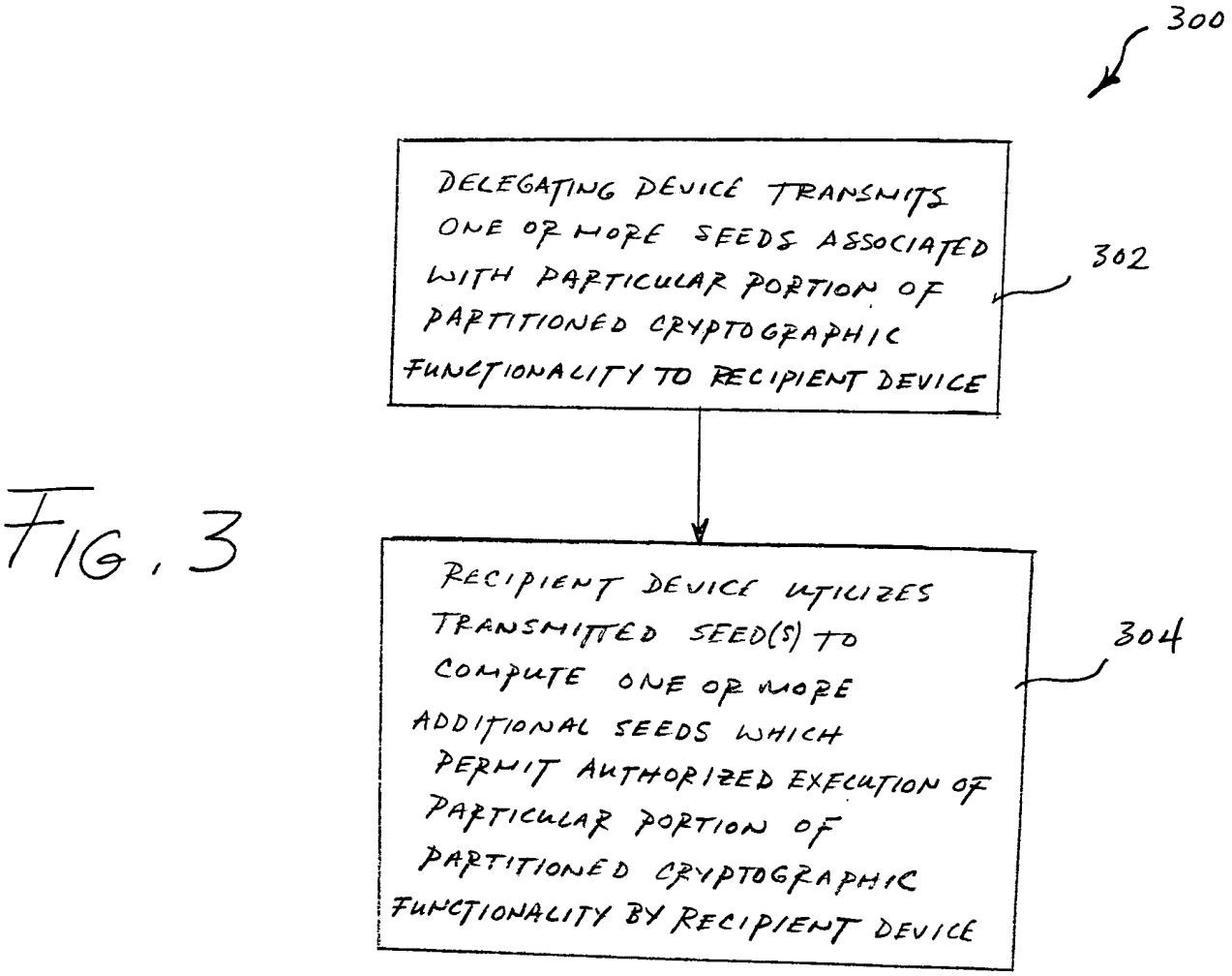
300
DELEGATING DEVICE TRANSMITS ONE OR MORE SEEDS ASSOCIATED WITH PARTICULAR PORTION OF PARTITIONED CRYPTOGRAPHIC FUNCTIONALITY TO RECIPIENT DEVICE
302
RECIPIENT DEVICE UTILIZES TRANSMITTED SEED(S) TO COMPUTE ONE OR MORE ADDITIONAL SEEDS WHICH PERMIT AUTHORIZED EXECUTION OF PARTICULAR PORTION OF PARTITIONED CRYPTOGRAPHIC FUNCTIONALITY BY RECIPIENT DEVICE
304

FIG. 3

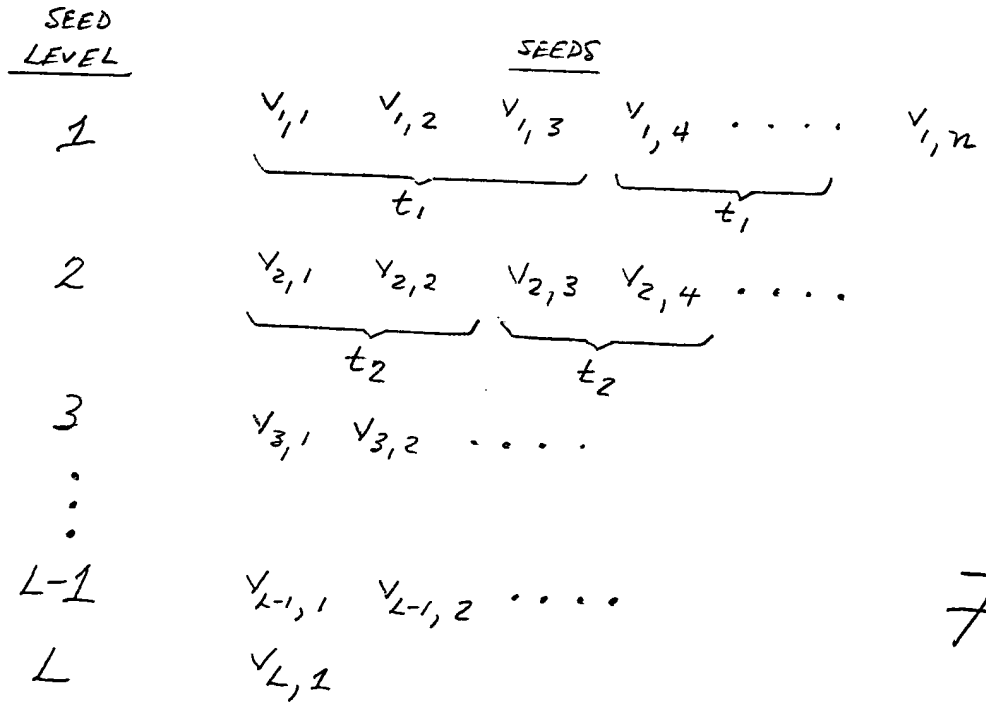
SEED LEVEL
SEEDS
1
$v_{1,1}$ $v_{1,2}$ $v_{1,3}$ $v_{1,4}$ . . . . $v_{1,n}$
t_1
t_1
2
$v_{2,1}$ $v_{2,2}$ $v_{2,3}$ $v_{2,4}$ . . . .
t_2
t_2
3
$v_{3,1}$ $v_{3,2}$ . . . .
L-1
$v_{L-1,1}$ $v_{L-1,2}$ . . . .
L
$v_{L,1}$

FIG. 4

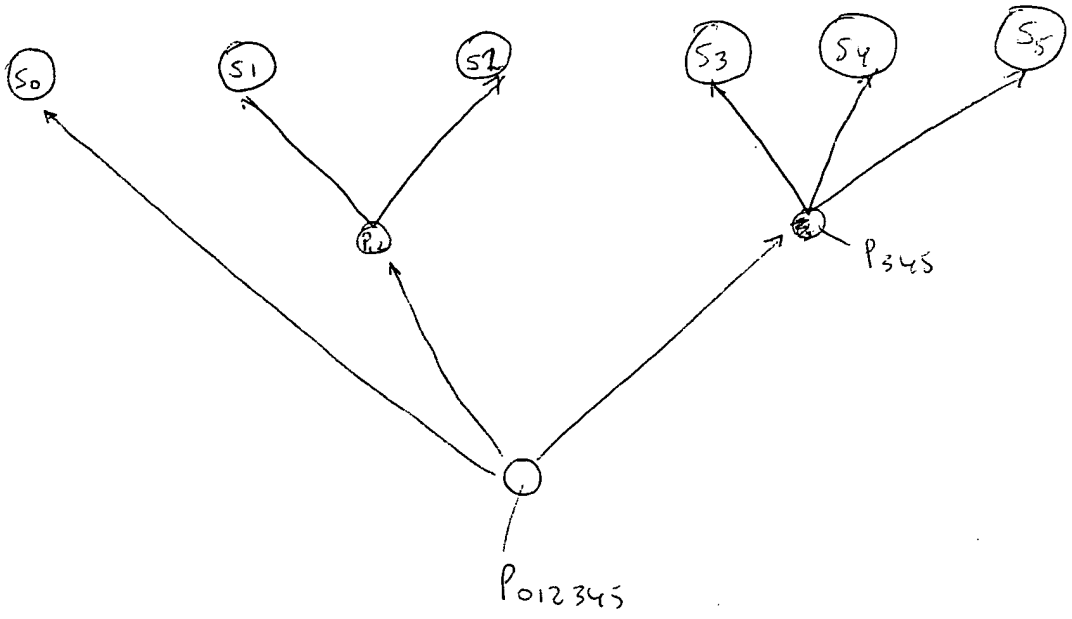

FIG. 5

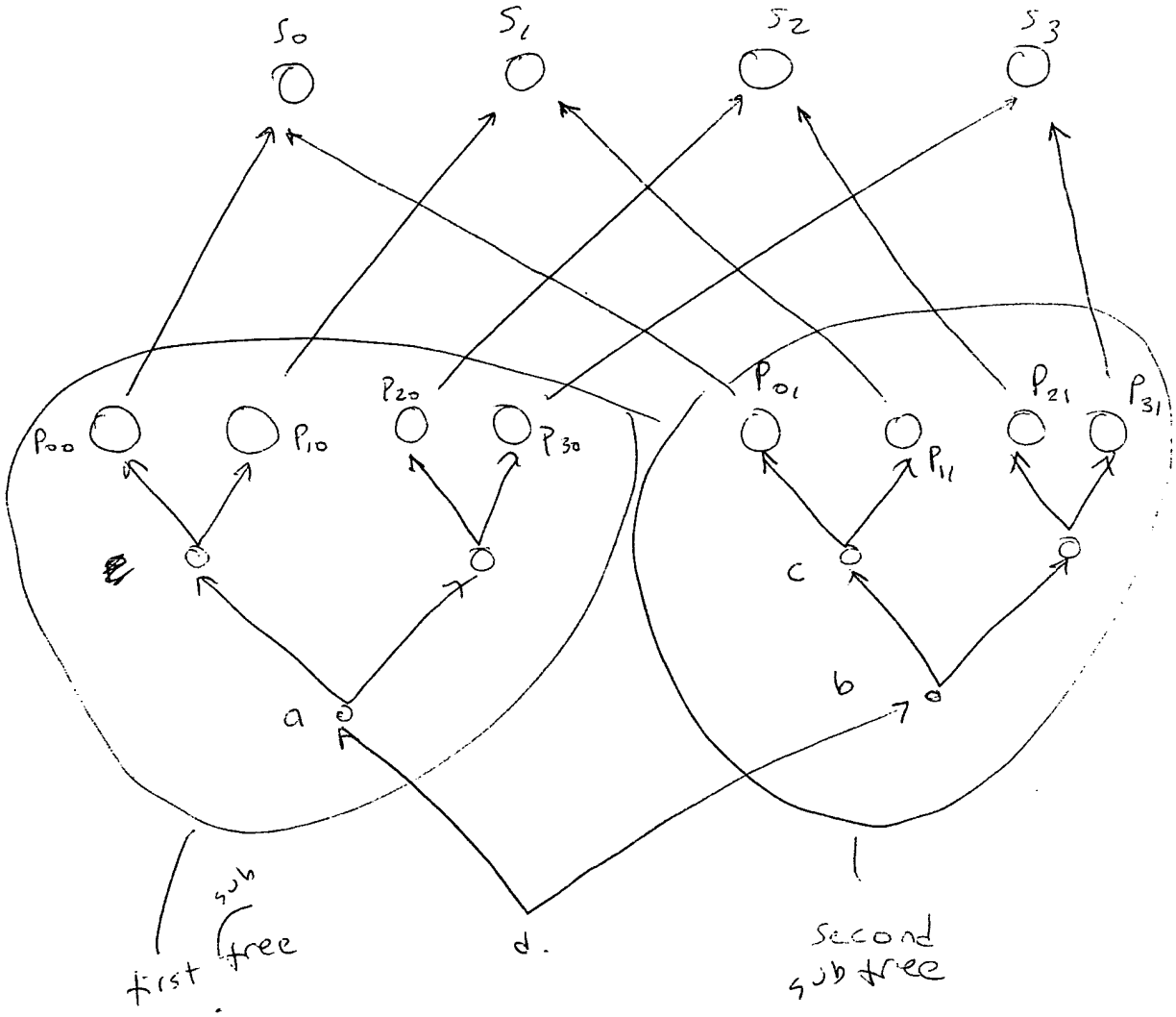
S_0
S_1
S_2
S_3
P_{00}
P_{10}
P_{20}
P_{30}
P_{01}
P_{11}
P_{21}
P_{31}
a
b
c
d.
first sub tree
second sub tree

FIG. 6

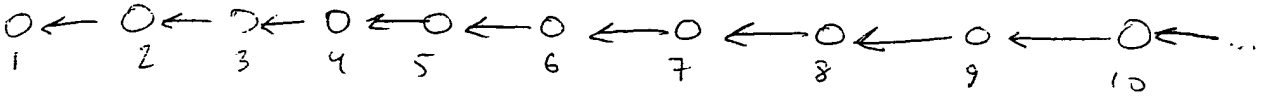
1
2
3
4
5
6
7
8
9
10

FIG. 7

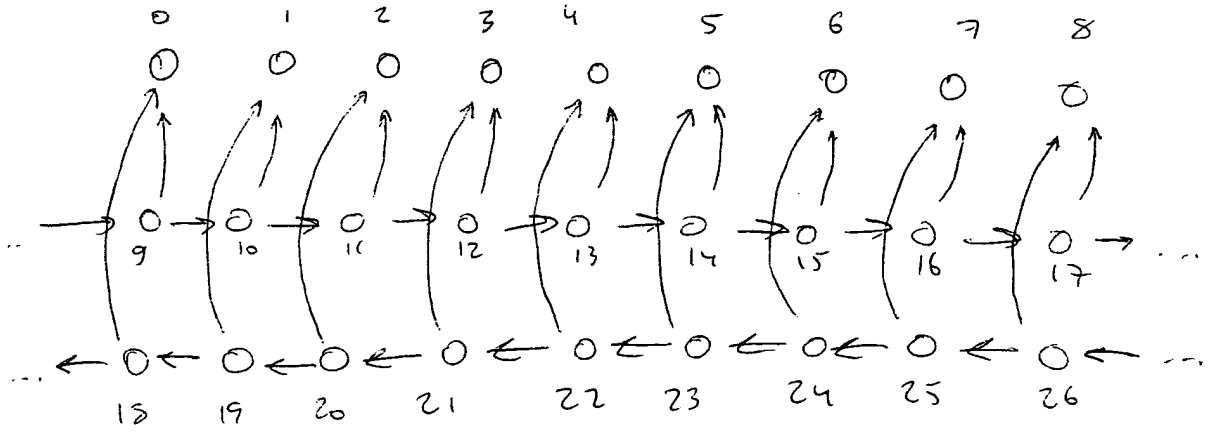
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26

FIG. 8

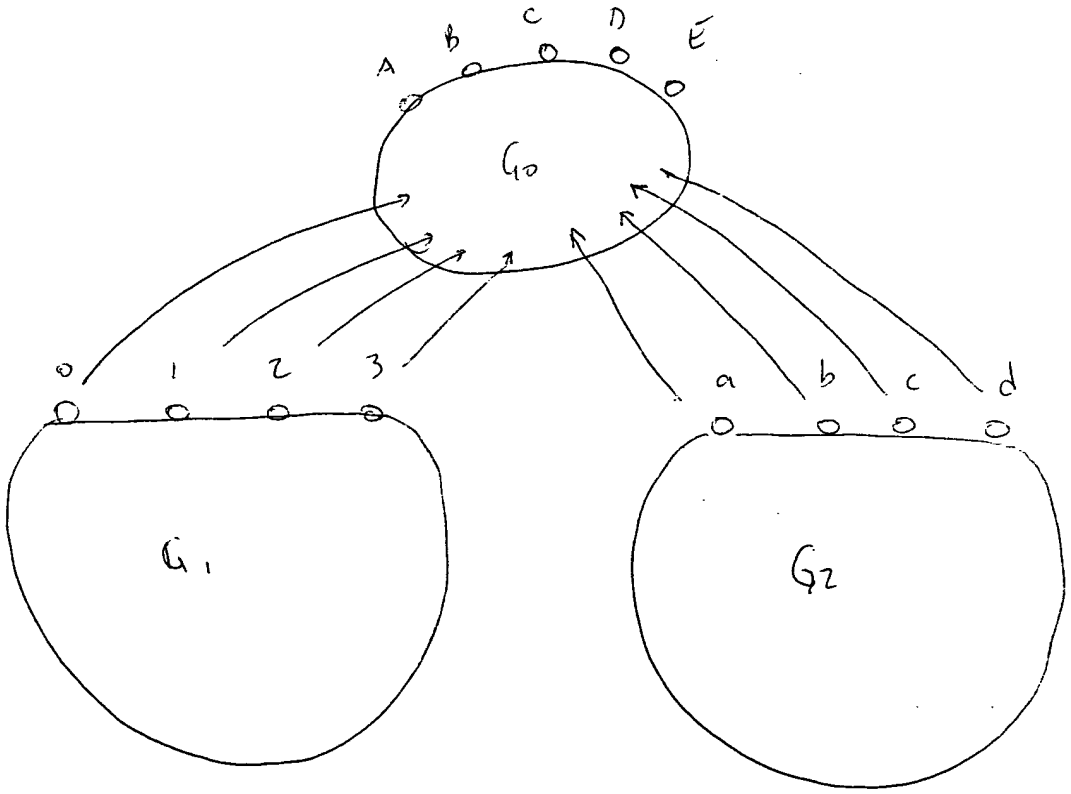

FIG. 9